

Before Reading This Manual

The "TLCS-900/H Databook (3)" is intended to provide the engineer designing and developing hardware or software of TLCS-900/L's each devices.

<Configuration>

This manual explains the CPU "TLCS-900/H CPU" in Chapter 3, then introduces contents of each devices in Chapter 4.

<Special Expression>

- (1) Explanation of a built-in I/O register: Register Symbol <Bit Symbol>

ex) TRUN<T0RUN> . . . Bit T0RUN of Register TRUN

<Reference>

The following are the reference data books required to develop the TLCS-900 Family.

- (1) For the TLCS-900 LSI devices

- TLCS-900 (1)

TMP96C141B,	TMP96C041B,	TMP96CM40
TMP96PM40,	TMP96C031Z	

- (2) For the TLCS-900/L LSI devices

- TLCS-900/L (1)

TMP93CS40/S41,	TMP93CM40,	TMP93PS40
TMP93CW40/W41,	TMP93PW40,	TMP93CS42A
TMP93PS42A,	TMP93CW46A,	TMP93PW46A
- TLCS-900/L (2)

TMP93CS32,	TMP93PW32,	TMP93CS44/S45
TMP93PS44,	TMP93CU44,	TMP93CW44
TMP93PW44A		
- TLCS-900/L (3)

TMP93CS20,	TMP93PW20A	
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- TLCS-900/L (4)

TMP93CW/CU/CT76,	TMP93PW76,	TMP93C071
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(3) For the TLCS-900/H LSI devices

- TLCS-900/H (1)
TMP95C061B, TMP95C063, TMP95C001
- TLCS-900/H (2)
TMP95CS64/265, TMP95CW64/W65, TMP95PW64
TMP95FY64, TMP95CS66
- TLCS-900/H (3)
TMP95CS54, TMP95PS54, TMP95CU54A
TMP95CW54A, TMP95FW54A

(4) For the TLCS-900/L1 LSI devices

- TLCS-900/L1 (1)
TMP91CU10, TMP91PW10
- TLCS-900/L1 (2)
TMP91CW12, TMP91PW12
- TLCS-900/L1 (3)
TMP91CW11, TMP91PW11

(5) For the TLCS-900/H2 LSI devices

- TLCS-900/H2 (1)
TMP94C241C, TMP94C251A
 - TLCS-900/H2 (2)
TMP94FU81
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